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MuelishIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/045,137 / Confirmation No.: None  
 First Named Inventor: Albert Z. H. Wang Filing Date: 23 October 2001  
 Group Art Unit: 2811 Examiner: Hu, S.  
 Atty. Docket No.: NS-3868-2C US National Docket No.: P03868 F1  
 Title: Dual Direction Over-Voltage And Over-Current IC Protection  
 Device And Its Cell Structure  
 Assignee: National Semiconductor Corporation

Santa Clara, California

COMMISSIONER FOR PATENTS  
 Washington, D.C. 20231

**POWER OF ATTORNEY BY ASSIGNEE OF ENTIRE INTEREST**

Sir:

The above-identified assignee, a Delaware corporation, hereby revokes all powers of attorney previously given and appoints the attorney(s) and/or agents(s) identified below to prosecute the above-identified patent application and to transact all business in the United States Patent and Trademark Office ("USPTO") in connection therewith:

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**ASSIGNEE CERTIFICATION UNDER 37 CFR 3.73(b)**

The undersigned representative of the above-identified assignee certifies that the above-identified assignee is the assignee of the entire right, title, and interest in the above-identified patent application by virtue of a chain of title from the inventor(s) of the above-identified patent application to the above-identified assignee as shown below:

An assignment (potentially executed in counterpart) from each inventor to the above-identified assignee on 17 June 1998 for U.S. patent application 09/100,384 whose priority is claimed in the above-identified application, the assignment having been recorded in the USPTO at Reel 9269, Frame 0250.

The undersigned (whose title is supplied below) is empowered to sign this certificate on behalf of the above-identified assignee.

Signature:  Date: 8/13/03

Name: Christopher J. Byrne

Title: Director of Intellectual Property and Technology Licensing

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